UP04601

Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For general amplification

Features

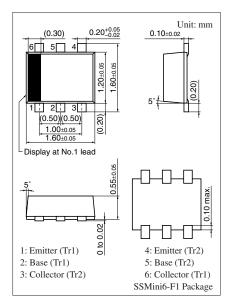
- Two elements incorporated into one package (Each transistor is separated)
- Reduction of the mounting area and assembly cost by one half

Basic Part Number

• 2SD0601A + 2SB0709A

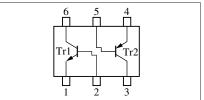
Absolute Maximum Ratings $T_a = 25^{\circ}C$

	Parameter	Symbol	Rating	Unit				
Tr1	Collector-base voltage (Emitter open)	V _{CBO}	60	V				
	Collector-emitter voltage (Base open)	V _{CEO}	50	V				
	Emitter-base voltage (Collector open)	V _{EBO}	7	V				
	Collector current	I _C	100	mA				
	Peak collector current	I _{CP}	200	mA				
Tr2	Collector-base voltage (Emitter open)	V _{CBO}	-60	V				
	Collector-emitter voltage (Base open)	V _{CEO}	-50	V				
	Emitter-base voltage (Collector open)	V _{EBO}	-7	V				
	Collector current	I _C	-100	mA				
	Peak collector current	I _{CP}	-200	mA				
Overall	Total power dissipation	P _T	125	mW				
	Junction temperature	Tj	125	°C				
	Storage temperature	T _{stg}	-55 to +125	°C				



Marking Symbol: 5C

Internal Connection



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\blacksquare Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

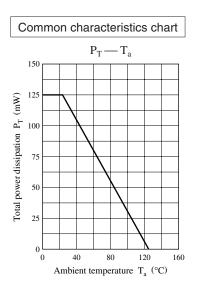
• Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{C} = 10 \ \mu A, \ I_{E} = 0$	60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = 10 \ \mu A, \ I_C = 0$	7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 20 V, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 10 \text{ V}, I_B = 0$			100	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 2 \text{ mA}$	180		390	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{C} = 100 \text{ mA}, I_{B} = 10 \text{ mA}$		0.1	0.3	V
Transition frequency	f _T	$V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		3.5		pF
(Common base, input open circuited)						

• Tr2

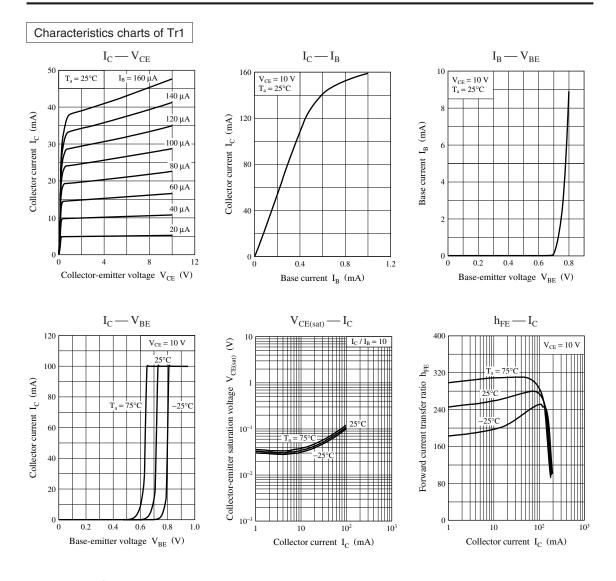
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{C} = -10 \ \mu A, \ I_{E} = 0$	-60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -2 \text{ mA}, I_{\rm B} = 0$	-50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = -10 \ \mu A, \ I_C = 0$	-7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$			- 0.1	μA
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = -10 \text{ V}, I_B = 0$			-100	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = -10 \text{ V}, I_C = -5 \text{ mA}$	180		390	—
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{C} = -100 \text{ mA}, I_{B} = -10 \text{ mA}$		- 0.3	- 0.5	V
Transition frequency	f _T	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		2.7		pF

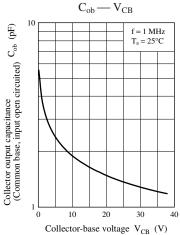
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.



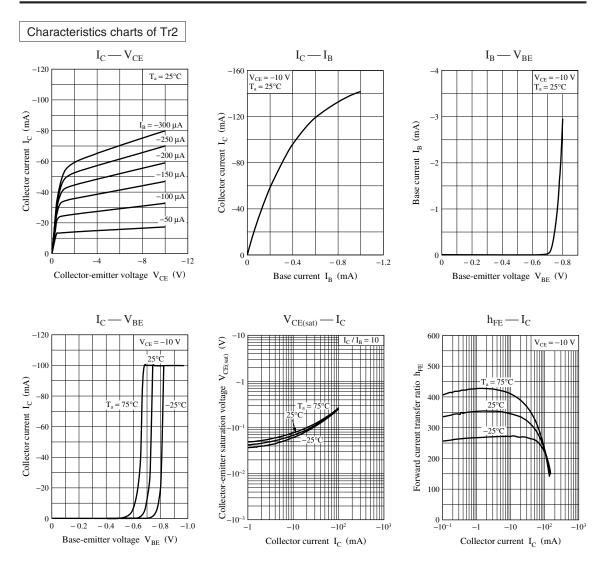
This product complies with the RoHS Directive (EU 2002/95/EC).

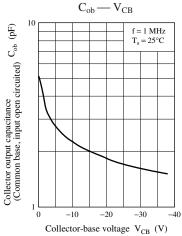
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